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# Pore size modulation in electrochemically etched macroporous p-type silicon monitored by FFT impedance spectroscopy and Raman scattering†

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The understanding of the mechanisms of macropore formation in p-type Si with respect to modulation of the pore diameter is still in its infancy. In the present work, macropores with significantly modulated diameters have been produced electrochemically in p-type Si. The effect of the current density and the amount of surfactant in the etching solution are shown to influence the modulation in pore diameter and morphology. Data obtained during the etching process by in situ FFT impedance spectroscopy correlate the pore diameter variation with certain time constants found in the kinetics of the dissolution process. Raman scattering and electron microscopy confirm the mesoscopic structure and roughening of the pore walls. Spectroscopic and microscopic methods confirm that the pore wall morphology is correlated with the conditions of pore modulation.

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### 1. Introduction

Macroporous Si with modulated pore diameters is of importance for a variety of applications. It supplies the base material for the preparation of self-standing Si microwires and Si microparticles,<sup>1-4</sup> photonic crystals,<sup>5-9</sup> and micropumps and membranes.<sup>10-12</sup> Additionally, it has been used in solar cell applications, 13-16 and as a template for the preparation of metal wires or tubes. 17,18 Most of the reports on macroporous Si with modulated diameters deal with n-type Si. Since the discovery of the etching process to make macroporous Si using n-type material and backside illumination in 1990, <sup>19</sup> considerable work has been dedicated to controlling and varying the structure and morphology of macroporous n-type Si. The etching mechanism has been understood, at least under standard etching conditions, 20-22 and there are also several publications detailing pore diameter modulations, including some reviews.<sup>23–25</sup> In strong contrast, reports on pore diameter modulation in p-type Si are rather scarce.  $^{1-3,26}$ 

Modulation of pore diameters in n-type Si under standard etching conditions (i.e. using low HF concentration of up to 4 wt%) can be easily obtained (and understood) by modulating the current density,22 taking into account the process of selforganization encountered<sup>27,28</sup> during etching. The so-called Lehmann model<sup>20</sup> is applicable in suitable ranges of the parameter space. The model assumes that for the etching process the current is mainly photocurrent, and that the current density at the pore tips is determined by the maximum current density  $j_{PS}$  found in the current-voltage characteristics of the system obtained under saturation photo-current conditions, with limited contribution from carrier diffusion along the pore-wall interface. The current density  $j_{\rm PS}$  is constant under the given conditions of temperature, etchant concentration, and depth. As the current density at the pore tips is constant, varying the total current i can control the pore diameters d since  $\pi (d/2)^2 j_{PS} = i$ . Good n-Si macropore formation implies that at the vicinity of the pore tip the pore walls are almost completely passivated, *i.e.* that a full SCR (space-charge region) is formed, but this is not always assured, however. The dissolution process induces charged surface states, which may reduce the width of the SCR and thus the focusing effect of the SCR, allowing thinning or over-etching of the pore walls. Since pore diameter modulations also modulate the speed of the pore growth into the bulk, the passivation kinetics should also be considered. Faster pore growth with increasing depth, for example, requires electrolyte additives (surfactants) that reduce dissolution kinetics by improved passivation.

In p-type Si, the modulation of the pore diameter is more complicated. In this case, the etching process is generally omnidirectional owing to the high hole concentration and

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faster hole mobility at the interface, and lateral pore growth is severely limited by the SCR between pores. 29-31 Since the availability of electronic holes necessary for the etching is much higher in p-type silicon than in n-type silicon, a selective passivation of pore walls is much more difficult to achieve. This necessary selective passivation is strongly influenced by several factors: (i) the crystal anisotropy, for example, {100} oriented pore walls are more susceptible to over-etching than {111} pore walls; (ii) overlapping SCR, enforced by prestructured nucleation sites with suitable geometry; (iii) strongly surface-passivating electrolytes; (iv) a large curvature around the pore tips, leading to a large electrical field at the pore tip. Experimentally it has been found that if current densities much lower than  $j_{PS}$  are used, it is possible to reduce the pore diameters by decreasing the current<sup>1</sup> during etching, allowing pore diameter modulation. Additionally, it has been reported that electrolyte additives like polyethyleneglycol (PEG) enhance attempted diameter modulations by improving the passivation of the pore walls.26

In this paper, the effect of the current and PEG additions on the pore diameter modulation of macropores in p-type Si is studied. Additionally, Fast Fourier Transformation Impedance Spectroscopy (FFT-IS), SEM microscopy and Raman scattering spectroscopy are used to elucidate details of the microstructure of the modulated macropores and the mesostructure of their pore walls.

# 2. Experimental details

Electrochemical etching of pores was performed on p-type Si wafers with resistivity (15–25)  $\Omega$  cm. Previous to the experiments a square array of inverted pyramids with basis size of 2.2  $\mu$ m  $\times$  2.2  $\mu$ m was formed on the wafers by contact lithography, reactive ion etching, and chemical etching in a KOH solution. The distance from center to center of the pyramids was 3  $\mu$ m in the  $\langle 100 \rangle$  directions. Further details on the patterning are given in ref. 1. Fig. 1 shows an example of the top view of the pores etched from these substrates.

The pore etching experiments were performed galvanostatically, using the current profile of Fig. 2a. The current profile consists of sections of two different constant currents (high and low). The "high current" was 20, 30, or 35 mA, while the "low current" was 3 mA for all the experiments. The etched area (surface area of Si in contact with the electrolyte) was 0.78 cm<sup>2</sup>. The high currents produce sections of wide pore diameter, while the low currents produce sections of narrow pore diameter, with diameters W and N, respectively. A schematic of the pores is shown in Fig. 2b. The etching electrolyte was composed of 220 mL N,N'-dimethylformamide (DMF) and 22 mL HF. Additionally 0, 1, or 2 g polyethyleneglycol 3600 (PEG, 3600 g mol<sup>-1</sup>) were added to the etchant. The temperature for the experiments was kept constant at 20 °C. The electrolyte was pumped through the etching cell (with capacity of 7 mL) at a rate of 600 mL min<sup>-1</sup> from a temperature controlled container that has a capacity of 400 mL. This allows

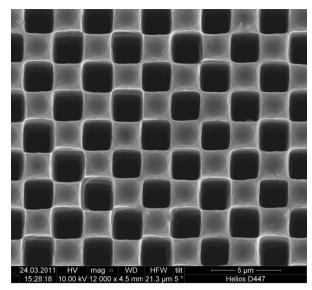
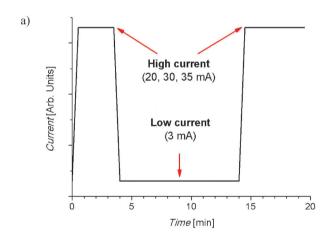


Fig. 1  $\;$  Top view of a typical macroporous sample prepared for this work. The distance from center to center of the pores is 3  $\mu m.$ 



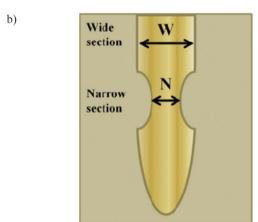


Fig. 2 (a) Current profile used for the electrochemical etching experiments. The current alternated between high current (20, 30, or 35 mA) and low current (3 mA). (b) Schematic of a pore, indicating the wide and narrow pore sections, with diameters W and N respectively; these sections are obtained with high and low currents respectively.

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Sample	High current [mA]	Amount PEG [g]	
20_0	20	0	
20_1	20	1	
20_2	20	2	
30_0	30	0	
30_1	30	1	
30_2	30	2	
35_0	35	0	
35_1	35	1	

a stable temperature in the etching cell, and avoids problems like electrolyte depletion. The reference electrode was positioned close to the samples to be etched (at around 2 mm distance), to keep the contribution of the electrolyte in the voltage and impedance measurements as small as possible. A simple schematic of the etching system can be found in ref. 32 and 33, with the difference that for the present experiments no backside illumination was needed. A list of the produced samples is shown in Table 1. In etching experiments employing 35 mA currents and 2 g PEG, the macropores were destroyed after reaching certain depths; the samples produced under these extreme etching conditions were not evaluated for the present work.

For the electrochemical etching experiments and FFT-IS measurements, a fully integrated laboratory etching system of ET&TE GmbH was used. FFT-IS spectra at 24 frequencies in a range from 5 Hz to 20 kHz were recorded in situ every 2 s. Microstructural characterization was done using an SEM microscope Helios D477 from FEI.

Raman scattering spectroscopy was conducted using a Horiba Jobin-Yvon Dilor XY Labram spectrometer with an Olympus BX40 confocal microscope. The excitation source was a 532 nm solid state 100 mW laser (filtered). The Raman spectra were collected using a Peltier-cooled CCD detector.

## 3. Results

#### 3.1. Pore structure

Fig. 3 shows samples etched without PEG for the three "high currents". The modulation of the pore diameters is observed in all samples. This confirms the idea that by varying the current densities one can vary the pore diameters, even in p-type Si, in contrast to previous reports, where an increase in the etching rate but no modification of the pore diameter<sup>30,31</sup> is observed. At first glance, one can see that both, the length and the width of the pores are affected by modulation of the current density.

Examples for visualizing the effect of PEG on the pore modulation are shown in Fig. 4. This figure shows SEM micrographs of pores prepared using 20 mA "high currents" and different levels of PEG.

By visual inspection it is possible to identify a good contrast between "narrow" (N) and "wide" (W) pore regions in all the samples, but it is clear that it is enhanced through the use of PEG. Shallower pores are typically found when using higher quantities of PEG additive. Plots of the diameters N and W as a

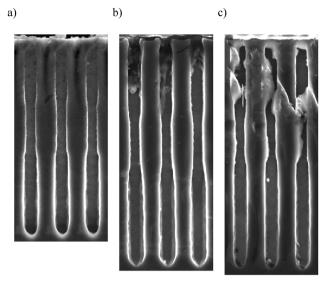


Fig. 3 Micrographs of pores etched with no PEG and (a) 20, (b) 30, and (c) 35 mA. The width of all the images is the same (12.7 μm), for a better comparison. The pieces in the pores of the sample etched at 35 mA are simply pieces of Si, due to an imperfect cleavage (the samples were cleaved for the observations).

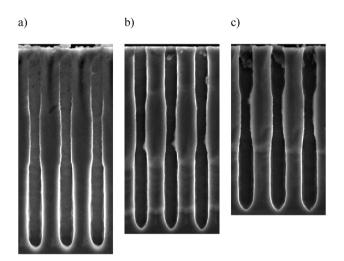


Fig. 4 Micrographs of pores etched with 20 mA and (a) 0, (b) 1, and (c) 2 g PEG. The width of all the images is the same (12.7  $\mu$ m), for a better comparison.

function of the PEG concentration for the different "high currents" are shown in Fig. 5. N and W vary continuously with the amount of PEG; the diameters mainly increase with a greater amount of PEG, except for N in the samples obtained with 20 mA "high current". In this last case N decreases by using more PEG.

A plot of the etching rates for etching the W sections prior to the development of the narrowing regions *N* is shown in Fig. 6. The etching rate increases with greater etching current, but the rate is retarded when greater amounts of PEG are added to the etching solution.

Fig. 7 shows high magnification SEM images of the W sections obtained at the different currents without PEG. Clearly the

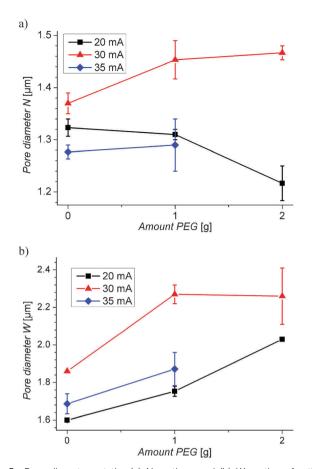


Fig. 5 Pore diameters at the (a) N sections and (b) W sections for the samples prepared with high currents equal to 20, 30, and 35 mA.

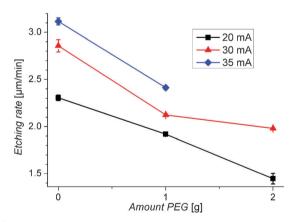
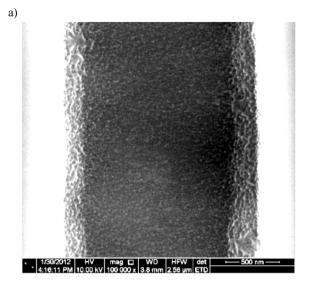


Fig. 6 Etching rate when preparing the W sections with high currents equal to 20, 30, and 35 mA, before the first narrowing.

roughness (meso-structure) increases with larger applied currents for a given pore diameter. On the other hand, the degree of pore wall texture becomes finer (the features are smaller, but denser) when increasing the amount of PEG. Fig. 8 shows an example of this effect for pores prepared with 30 mA without PEG and also with 2 g PEG. The same effect is found for the samples prepared with a high current of 20 mA, but it is different for the ones prepared with 35 mA. The addition of



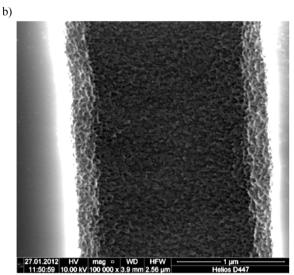


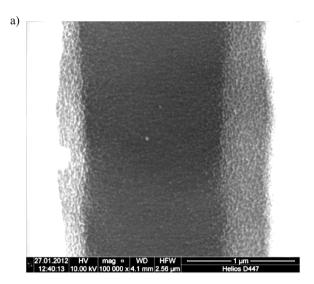
Fig. 7 Micrographs of the pore wall surfaces at wide sections of samples etched without PEG and (a) 20 and (b) 35 mA. The level of roughness increases when increasing the etching current.

1 g of PEG at the highest etching current of 35 mA causes some shallow cavity formation on the pore wall.

SEM images in high magnification of all the samples are provided as ESI† (S1). All these tendencies are represented graphically in Fig. 9, where a qualitative plot of the roughness versus the amount of PEG is shown for every high current, determined through visual inspection analysis of the microscopy data.

To get a quantitative measure of the roughness and texture change as a function of the PEG passivation and its influence on pore wall etch rates, Raman scattering was used.

Raman scattering is commonly used for estimating Si crystallite sizes, and in the case of roughness it is a very powerful technique for probing texture effects on phonon scattering at roughened interfaces and crystalline boundaries.34-36 Typically, the width and asymmetry caused by different phonon



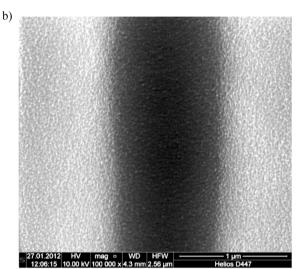


Fig. 8 Micrographs of the surface of the pore walls at wide sections of samples etched with 30 mA current and (a) no PEG, (b) 2 g PEG. The roughness gets finer (smaller and denser features) by adding PEG.

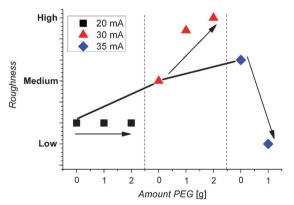


Fig. 9 Qualitative plot of the roughness versus the amount of PEG is shown for every high current. The lines are an aid for a better visualization.

scattering and dispersion are most evident in the transverse optical phonon (TO) mode,  $^{37-40}$  which is centered at  $\sim 520$  cm<sup>-1</sup>

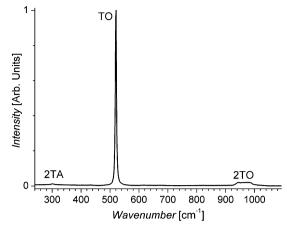


Fig. 10 Typical Raman spectrum of the studied samples. The common Raman peaks of crystalline Si can be identified.

for crystalline Si. As the crystallite size associated with the mesoscale roughness or with the micro-crystalline Si decreases, the Raman shift and peak width increase and the line-shape becomes asymmetric with an extended tail at lower energies for phonon confinement effects. 41-43 A typical Raman spectrum of the samples described above is shown in Fig. 10. As can be seen in Fig. 10, the TO peak at  $\sim$  520 cm<sup>-1</sup> and the two phonon 2TO mode at  $\sim$ 960 cm<sup>-1</sup> can be detected, as well as a peak at  $\sim 300 \text{ cm}^{-1}$ , which is the two phonon 2TA mode of Si. The intensity of this last peak is two orders of magnitude smaller than the TO peak, as is the case of crystalline Si. This mode would be enhanced just if an amorphous Si phase is present, where also LA phonons are enabled;44 thus the samples are crystalline and any passivation or randomization/amorphization of the effective surface by roughening is negligible, i.e. the rough features on pore walls are remnant Si left from etching, and not individual crystallites.

In a relatively recent report, the asymmetry of the TO peak has been used for determining the crystallite sizes;<sup>45</sup> this method is independent of the absolute height and width of the Raman peak. For the present study, as the differences in

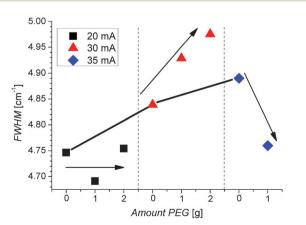


Fig. 11 FWHM of the TO Raman peak of the different samples, versus the amount of PEG used for the etching, for the different etching currents. The lines are an aid for a better visualization.

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asymmetry between samples are in the same range of the error upon considering the center of the peak, this method is not considered. The full width at half maximum of the peaks (FWHM) was considered for this study. In Fig. 11 a plot of the FWHM *versus* the amount of PEG used for the etching, for each of the high currents, is shown. When comparing the samples prepared with no PEG, one can see that the FWHM widens at increasing the etching current.

On the other hand, the FWHM exhibits different tendencies depending on the applied current, when increasing the amount of PEG (see the arrows in the figure). These are the same tendencies observed in Fig. 9, for the qualitative analysis of the roughness of the samples (nanoscale features lining the inner walls of the macropores). Nanoscale features like the described roughness typically tend to result in some degree of phonon confinement, but also contribute to asymmetry in the line shape from surface scattering effects. In practice, one observes changes in the Raman spectrum related to size effects via phonon confinement just when the crystallites are smaller than 100 nm (wavevector dependent); it is thus very plausible that the changes in FWHM described in Fig. 11 are related to the roughness of the pore walls which result in diffusive rather than specular phonon scattering<sup>46</sup> at the rough interface, corroborating the qualitative analysis of Fig. 9. The roughness increases with the applied current when no PEG is used, and the effect of PEG on the roughness varies with applied current.

#### 3.2. Pore structure

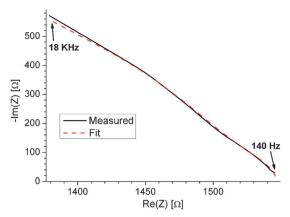
FFT-IS is a powerful technique for the electrochemical characterization and physicochemical modeling of electrochemical systems. This technique can be applied *in situ*, since it has the capability of measuring an impedance spectrum in very short times, usually for durations <1 s. With the aid of FFT-IS major insights into the pore etching process have already been obtained, especially for n-type Si etched with backside illumination on type InP. Si,55,56 Additionally, the results on p-type Si are encouraging.  $^{26,57}$ 

The data obtained from the impedance measurements during the electrochemical etching of the samples can be satisfactorily fitted with an equivalent circuit model containing a series resistor ( $R_{\rm s}$ ) and 3 time constants ( $\tau_1$ ,  $\tau_2$ , and  $\tau_3$ ) that are coupled with the resistors  $R_1$ ,  $R_2$ , and  $R_3$ . This model agrees with the previous reports on electrochemical etching of macropores in p-type Si.<sup>26,57</sup> The impedance Z of the model can be represented mathematically by

$$Z = R_{\rm s} + \frac{R_1}{(1 + i\omega\tau_1)} + \frac{R_2}{(1 + i\omega\tau_2)} + \frac{R_3}{(1 + i\omega\tau_3)}$$
 (1)

where  $\omega$  is the angular frequency.

An impedance spectrum was obtained every 2 s during the etching. Every spectrum obtained was fitted to eqn (1) and the parameters were extracted in real time. An example of the fit is shown in Fig. 12. One of such fitted spectrum was obtained every 6 s during etching. The results of the fit parameters were plotted as a function of time. Fig. 13 shows the values of the time constants for the sample etched with 30 mA high current



**Fig. 12** Example of fit of impedance data (sample etched with 20 mA high current and 1 g PEG). One such fitted spectrum was obtained every 6 s during etching for all the samples.

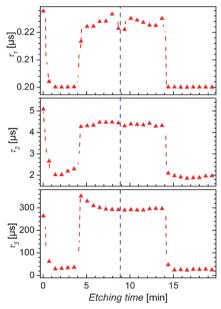


Fig. 13 Time constants  $\tau_1$ ,  $\tau_2$ , and  $\tau_3$  of the etching process for the sample prepared with 30 mA high current and 2 g PEG. The blue line indicates the center of the time at "low current".

and 2 g of the PEG additive. Here, the parameters that predominantly follow the form of the current profile (see Fig. 2a) are the time constants. The other parameters do not show an apparent correlation.

As in ref. 26, where PEG was used as an additive, in the present work it is considered that  $\tau_1$  is related to the etching at the pore tip, which is nominally the fastest process assuming that the electrochemical reaction dominates over carrier diffusion at the interface since this effect is insignificant for p-type Si where holes are majority carriers with a large bulk concentration.  $\tau_2$  is considered to be related to the dissolution of the pore walls, which can occur through hole supply at the wall interface (from p-type silicon). Since holes are readily available in p-silicon, carrier diffusion is less dominant and the etching current at any point away from the tip is a function of the radius

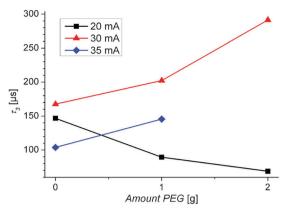


Fig. 14 Time constant  $\tau_3$  for the samples prepared with high currents equal to 20, 30, and 35 mA. The values were taken at the middle of the time for etching the narrow sections.

of curvature of the pore walls. As this radius of curvature is much larger than the apex of the pore tip, the etching process at the walls is comparatively slower. For  $\tau_3$  it has been observed that it varies with the amount of PEG, as depicted in Fig. 14, where the value of  $\tau_3$  is plotted *versus* the amount of PEG, for every high current. The origin of  $\tau_3$  can be ascribed to the diffusion of the electrolyte during the etching process, and more directly to the diameters of the pores, as will become clear in the section for Discussion. In ref. 26 it was not clear which was the origin of this time constant, but it was related to certain "transitions" of carriers from the pore tips to the PEG-passivated walls. That model fits good with the practical observations when using high currents in the range of 20 mA, but not with the data obtained using higher currents.

# 4. Discussion

#### 4.1. Pore structure

**4.1.1. Effect of the current.** From Fig. 5a and b it is possible to see that both diameters of the pores depend on the three "high currents" chosen – but the relationship is not linear. Fig. 15 shows the data from Fig. 5a and b of the samples prepared without PEG, to demonstrate this.

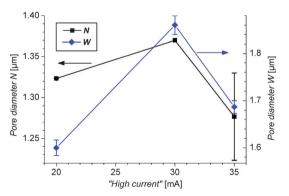


Fig. 15 Pore diameters at the N and W sections of the samples prepared with "high currents" equal to 20, 30, and 35 mA with no PEG.

It can be seen from this figure that W increases when going from 20 to 30 mA high current, but decreases with a further increase of the current to 35 mA. This can be explained as follows: at currents up to 30 mA the etching of Si occurs with the hole provided from the bulk at the pore tip and at the walls. The highest current of 35 mA corresponds to the initial peak current (density) corresponding to electropolishing, current densities larger than  $j_{PS}$ . For this process, tunneling of holes through the (thin) space charge region occurs in addition to diffusion. The tunneling process occurs preferentially at the pore tips, due to the narrower space charge region caused by the high tip curvature. Thinner and longer pores propagate when the majority of the charge transport is localized at the pore tip, 58,59 and the overall shape of the pore is influenced by the rate of reaction at the pore tip *versus* that occurring at the pore walls. In the present case the etching rate in the axial direction increases when increasing the high currents to 35 mA (see Fig. 6), while the pores become thinner when etching with this current.

The diameter N is also affected by the "high currents", as seen in Fig. 15. This is remarkable since N could be expected to be the same for all the samples prepared with the same amount of PEG, independently of the intensity of the "high current", since these sections are produced during the application of the always identical "low current". Nevertheless, as the process for over-etching the W sections is relatively isotropic, the already etched N sections would also get over-etched at this stage. For this reason, the pore widths of the N sections follow a similar trend as the W sections when increasing the high currents.

**4.1.2. Effect of polyethyleneglycol passivation.** PEG acts as a passivating agent during the etching process. As discussed in the Introduction, the "passivation" concept relates to the reduction of the chemical activity at a certain interface, and can be accomplished in different ways. It mainly refers to the effect and not to the cause. PEG can be qualified as a passivating agent, since it adheres to the pore walls reducing the etching rate (see Fig. 6). The concomitant reduction of the etching rate is unlikely to be related to a diffusion limitation mechanism, since it happens even at the upper part of the pores, where there is plenty of electrolytes and diffusion into the pore depth not yet an issue.

The dependency of the pore diameters W on PEG additions can be observed in Fig. 5b. With additions of PEG the pore walls and the pore tip become more passivated, which reduces the etching rate at the pore tip and makes the etching more isotropic, *i.e.* lateral growth and depth growth rates are less different, thus the pores become wider. For the pore diameters N the same effect can be observed, except when etching with 20 mA current. For this current the over-etched region may be minimum (there is no constraint of availability of holes at the pore tips for this low current), and it may be even smaller when increasing the amount of PEG, since the pore walls may be better passivated. In this way, the N sections are less overetched, and thus, they are thinner.

The increasing roughness of the samples by increasing the high current or the amount of PEG can explain the level of passivation at the pore tips, which increases/reduces the chance of modulating pores. PEG offers some degree of

passivation, but clearly does not eliminate charge transfer completely. PEG could potentially retard the rate of etching for uniform PEG coverage, or in the case of uneven coverage of the pore wall, only allow etching at uncoated sites which still amounts to the lowest rate of etching and thus current. A clear correlation between the ability of a PEG containing electrolyte to allow for significant pore diameter modulation and the meso-structure of the surface of the pore walls is found at least for not too high current densities: the higher the PEG content, the finer is the meso-structure at the pore walls and the more pronounced is the pore diameter modulation induced by steps in the etching current density. Just in the samples prepared with 35 mA the roughness decreases when increasing the amount of PEG. A current of 35 mA may approach the limit of electropolishing, which may be enhanced with PEG. Overall, given the high density of varied crystallographic surface terminations on the rough pore walls, as opposed to the nominal {100} planes associated with the square profile of the walls into the bulk of the semiconductor, the PEG passivation appears to be effective by coating multifaceted or rough semiconductor surfaces.

#### 4.2. Indication of diameter modulation by FFT-IS

As can be observed from Fig. 14, there is a strong influence of the amount of PEG in the etching solution on the value of the time constant  $\tau_3$  of the etching process, calculated from FFT-IS data. The dependency is direct (a correlation with positive slope is observed), except for the samples prepared using a high current of 20 mA. A similar tendency is exhibited by the N sections of the pores when increasing the amount of PEG (see the tendencies of N in Fig. 5a). This may be an indication that  $\tau_3$  depends on the diameter of the narrow sections. A plot combining the data of Fig. 5a and 11 is shown in Fig. 16. It can be seen from the figure that  $\tau_3$  can have a direct (30 and 35 mA) or inverse (20 mA) relation with the amount of PEG (indicated with arrows), but it has a direct relation with the diameter of N (indicated with the dashed line). An increment in the diameter of N implies an enhancement or constraint for the diffusion of the etching solution. Impedance spectroscopy correlated with

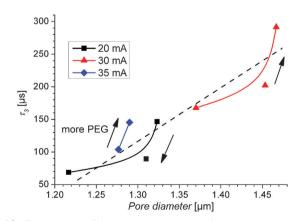


Fig. 16 Dependency of the time constant  $\tau_3$  on the pore diameter at the narrow sections. For the samples prepared with high currents equal to 20, 30, and 35 mA. The lines are an aid for a better visualization

the microscopy measurements leads to the possibility that  $\tau_3$  is likely related to the time constant of the diffusion process. This is very plausible, since this time constant is two orders of magnitude slower than any of the other two time constants, which are related to electron transfer processes.  $\tau_3$  could be used for estimating the diameter of narrowing pores during their diameter modulation.

### 5. Conclusion

Macropores with modulated pore diameters have been successfully produced electrochemically in p-type Si. Polyethyleneglycol (PEG) has been used as an additive for the etchant in different amounts.

The etching current has also a direct influence on the pore diameter. At low and moderate currents a direct dissolution of Si occurs, given the electron holes at the pore tip and at the walls; in this current range the pores get wider with increments in current. At currents close to the range of electropolishing, tunneling of carriers occurs, which is preferential at the pore tips; this makes the pores narrower again.

The diameter of the narrow and the wide pore sections varies continuously with the amount of PEG. The diameters mainly increase with a greater amount of PEG additive, except for the narrow pore sections where the over-etching produced during the production of the wide sections is minimum (when moderate currents are used). The pore walls and the pore tip become effectively passivated with PEG. This passivation reduces the localized etching rate at the pore tip and makes the etching more isotropic (the walls are consumed more). The roughness of the pore walls, measured by monitoring the variation in phonon scattering at the roughened surface, correlates well with the effect of passivation on the etching mechanism, and consequently, with the ability to modulate pores and modify the pore wall texture.

Different time constants of processes occurring during etching can be calculated from FFT impedance spectroscopy data. In this work a correlation of one of the time constants with the diameter of the narrow sections was observed. It may be possible to detect the variations in diameter of the narrow sections by FFT impedance spectroscopy.

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