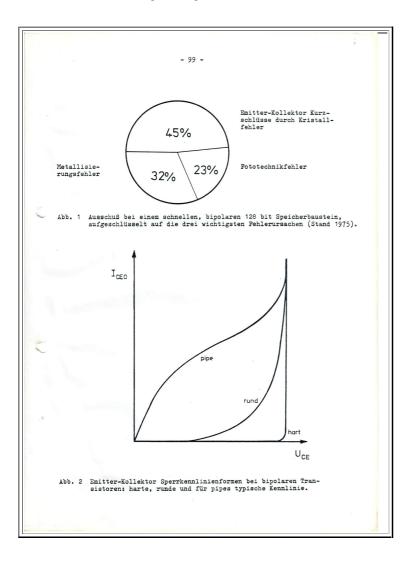
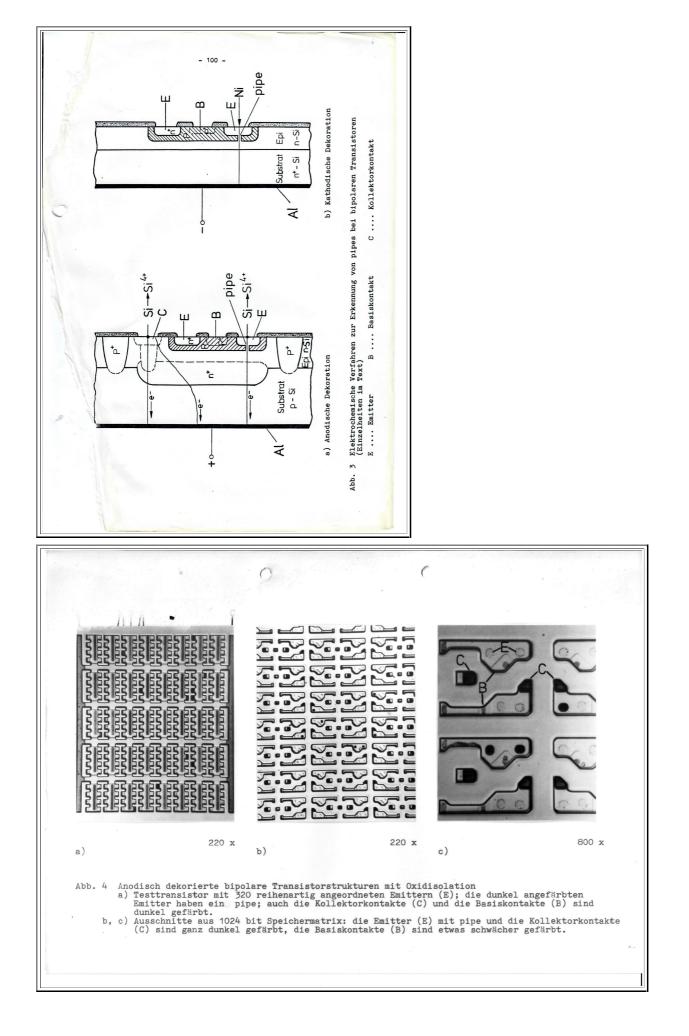
Pictures to: Kristallfehler in hochintegrierten Schaltkreisen aus Silizium Part 1

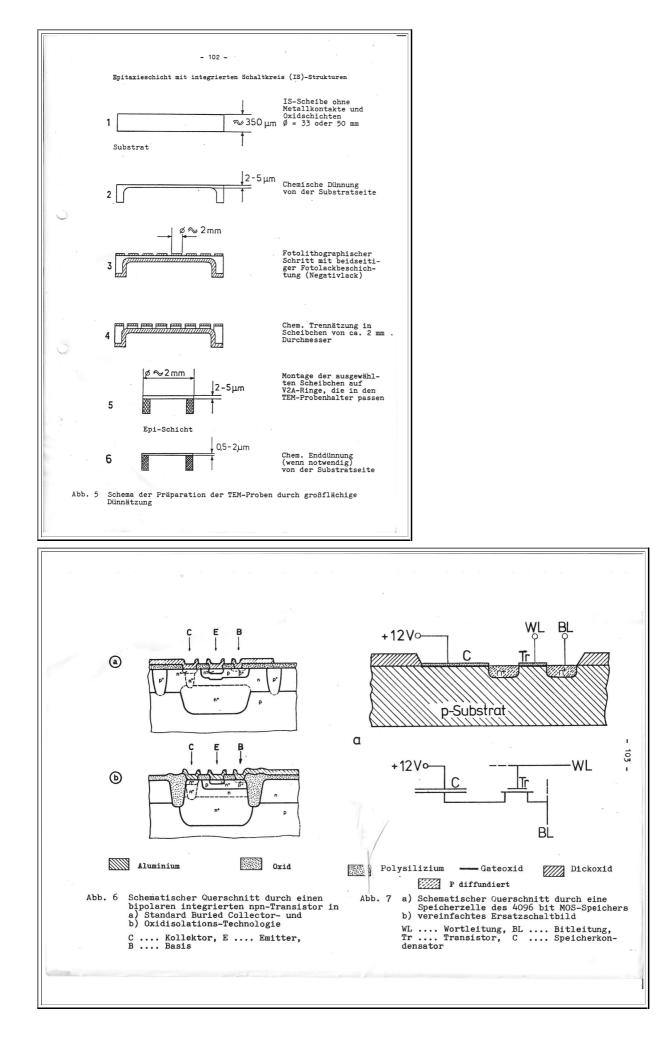
(Crystal Lattice Defects in Highly Integrated Silicon Devices)

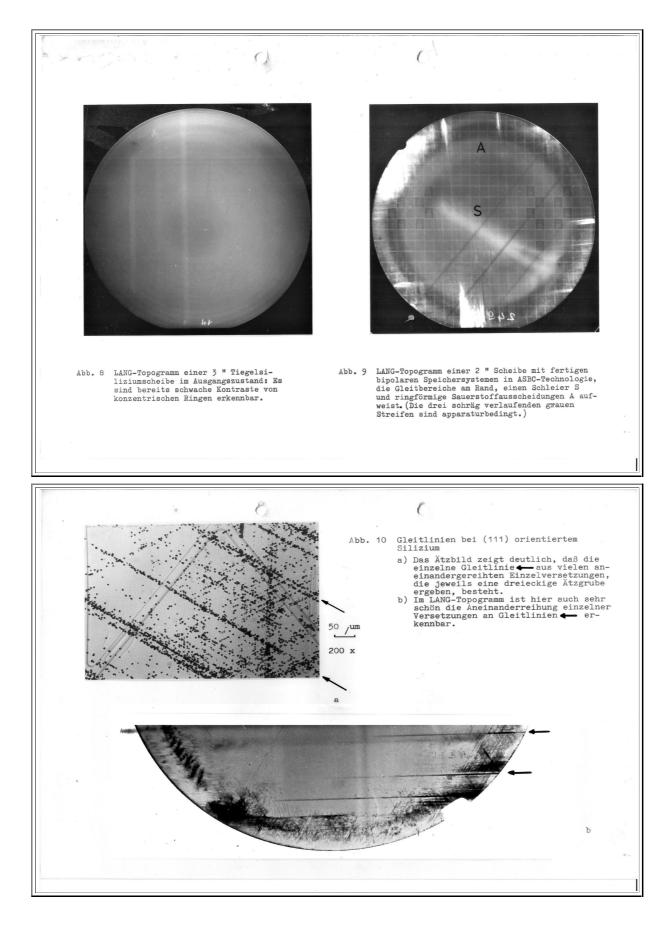
In what follows you see the second part of the "Abbildungen" (Pictures) as they occur in the report. The scans were made from my still existing copy and some of the pictures were somewhat processed by me to improve clarity.

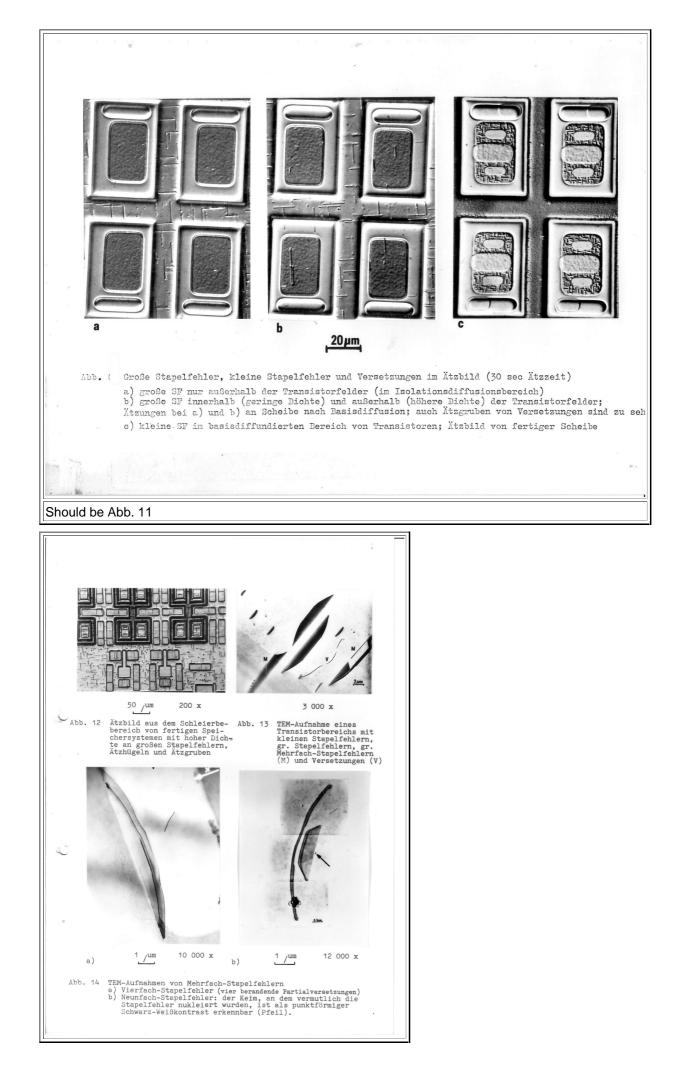
The figure captions are included so there is no need for further text. Here we have Fig. 1 - Fig. 21

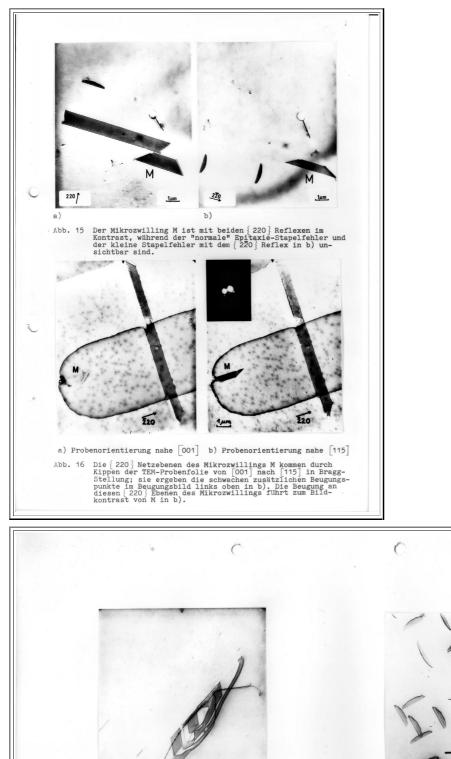












10 000 x

/um 1

+ 400 2,um 5 000 x Abb. 17 Komplizierter Mehrfachstapelfehler im bordiffundierten Isolations-rahmen (TEM) Abb. 18 Kleine ca. 2 /um tiefe Stapelfehler mit Segelbootform (Pfeile) in basis-diffundierten Bereichen (TEM)

